

IN THE CLAIMS:

Claims 5 through 24 were previously cancelled by Examiner's Amendment. None of the claims have been amended herein. All of the pending claims are presented below. This listing of claims will replace all prior versions and listings of claims in the application. Please enter these claims as previously amended.

Listing of Claims:

1. (Previously presented) A method for performing at least one operation of a testing operation and a burn-in operation on an uncut wafer having two opposing surfaces using a first plate which includes a biasing mechanism, and a second rigid plate selectively engageable with the first plate, the second rigid plate having a plurality of contact elements extending therefrom and having a cavity for receiving the uncut wafer therein, the method comprising:
providing a wafer having a plurality of semiconductor die thereon;
placing the wafer, in a desired orientation, between the first plate and the second rigid plate with the plurality of contact elements on the second rigid plate engaging corresponding locations on the wafer, the second rigid plate receiving the wafer therein; and
biasing the plurality of contact elements of the second rigid plate against a surface of the wafer by applying an elastic force to an opposing surface to the surface of the wafer with the first plate, the force caused by the biasing mechanism, and the force applied to substantially the entirety of the opposing surface.
2. (Original) The method as claimed in claim 1, wherein the wafer comprises a semiconductor wafer.
3. (Original) The method as claimed in claim 1, wherein the wafer includes an array of semiconductor dice located thereon.

4. (Original) The method as claimed in claim 1, further comprising:
aligning the first plate and the second rigid plate.

5.-24. (Cancelled)